## **Power Transformer Design Checklist**

Company Name:		Date:		
Contact Person:		Address:		
Tel.:		Email:		
	□New Project			
Application:	□Replacement <sup>1)</sup> Type:			
	Application <sup>2)</sup> :			
	<b>_Welder</b> :	Project Name,Description:		
Safety	□Railway:			
Requirements (please specify	□UPS:			
standards,use p.2):	☐Aerospace:			
	☐Other:			
	Current Year:	Target Price [\$]:		
ExpectedAnnual	Next Year:			
Usage [pcs]:	Year 3:	Product Life Cycle [Years]:		
	Year 4:			
SamplesQuantity:	pcs.	Desired SampleDate:		
QM-Requirement:	QM-Requirement:			
1) e.g. NiFe core, ferrite core. 2) e.g. power supply, inverter, solar, drive, UPS, welding.				
Circuit Topology <sup>3)</sup>				
☐Forward Transformer ☐with demagnetization winding ☐with 2 switching transistors		∐ταιι bridge □ half bridge	☐Flyback Transformer ☐trapezoidal current ☐triangular current	

<sup>3)</sup> Use p. 2 to draw circuit diagram. 4) Two primary side windings.

Transformer Secondary Windings				
No. of Windings:		Specify Center Taps (use p.2)		
Output Voltage(s) with Load	//	Output Current(s) with Load	//	
[Volt, 1/2/3/4/5/6]:	//	[Ampere, 1/2/3/4/5/6]:	//	
Insulation & Environmental Requirem	nents			
Insulation Type:	☐Basic ☐Reinforced ☐Other:			
Insulation Voltage PRI–SEC [Vrms]:	□None □EN: □UL:			
Electrical Standards:	□Others:			
System Voltage (e.g. 230V,390V) [Vrms]:		Partial Discharge Voltage PRI-SEC [Vrms]:		
Working Voltage (across xfmr) [Vrms]:		Partial Discharge Voltage SEC-SEC [Vrms]:		
Recurring Peak Voltage [V]:		Partial Discharge Voltage PRI - Casing [Vrms]:		
Pollution Degree (Typically 2):		Other:		
Environmental Requirements:	Vibration: Hunidity: Dust:			
Important Operational Characteristics				
Nominal Power [W]:		Operating Frequency[kHz]:		
Input Voltage Range [Volt]:	to	Maximum Pulse Width [%]:		
Ambient Temperature Range [°C]:	°C to°C	Convection Cooling:		
Max. Operating Temperature [°C]:		Forced Cooling:	□ [K/W]	
Max. Dimensions: W × D × H [mm]:	xx	Heat Sink:	□ [m/s]	
Desired Contacts:	□None (Tinned Leads) □ □Cable Lugs □□Other:			
Lead Length[mm]:				

Additional Specifications	

Test Voltage PRI-SEC [Vrms] /Time [s]:	
Test Voltage SEC-SEC [Vrms] /Time [s]:	
Core (if already defined):	

Circuit Diagram:

**Additional Information:**